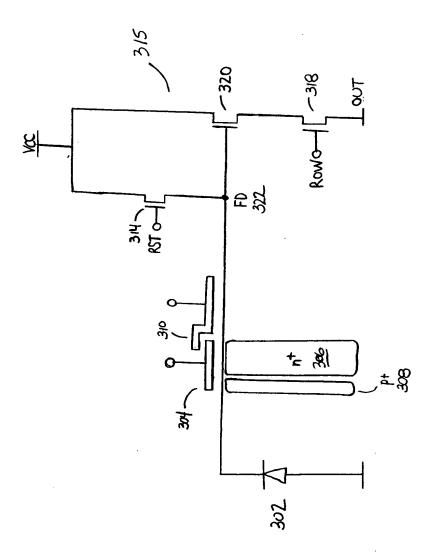
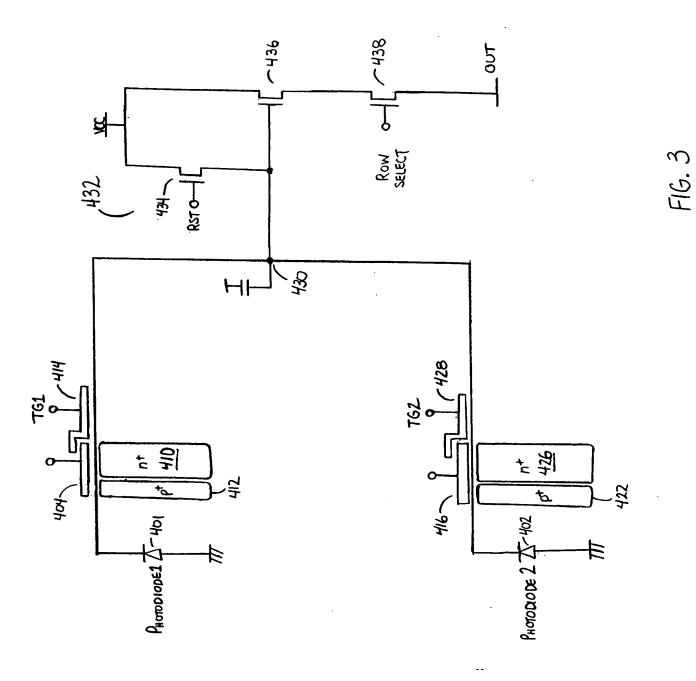
280 275 2,67 Vsig\* Vrst, Vsig (Col) - 908 265 FIG. 1 Prior Art 260 200 210 250

Image / output





F16.2



F16. 4

Photodiode 302 Reset and Storage Node 306 Charge Storage Timing

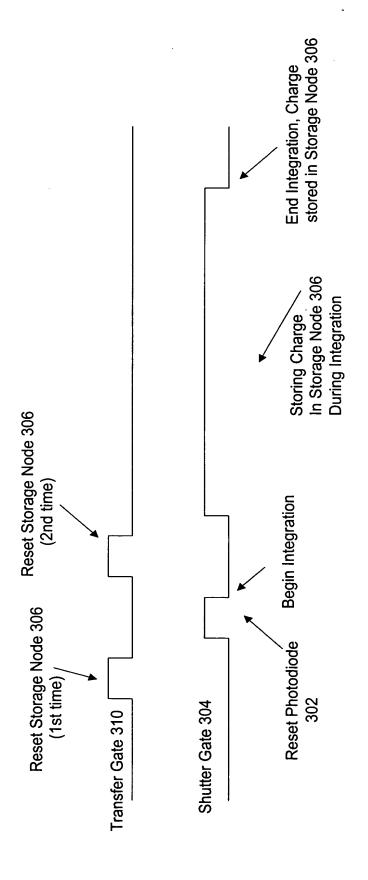


FIG. 5

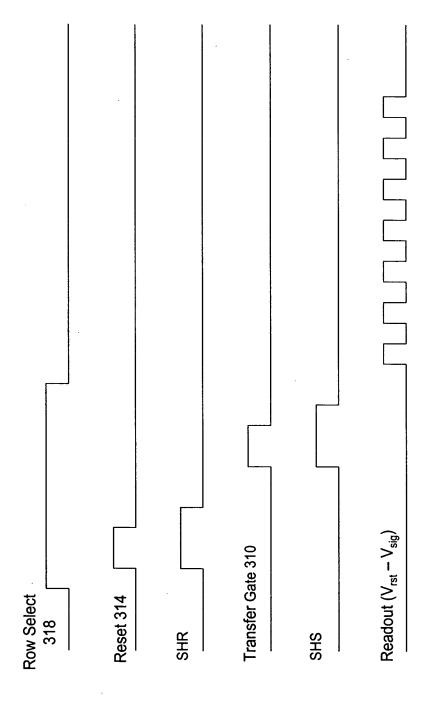


FIG. 6

Circuit 400 Pixel Output Timing

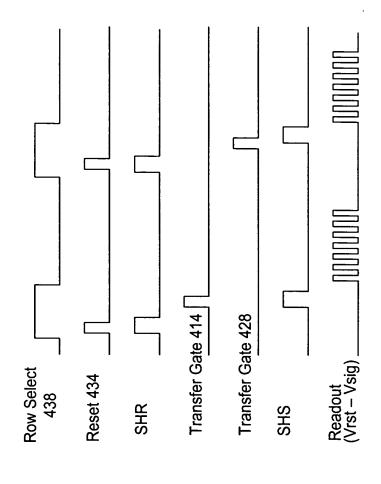


FIG. 7

**Circuit 500 Pixel Output Timing** 

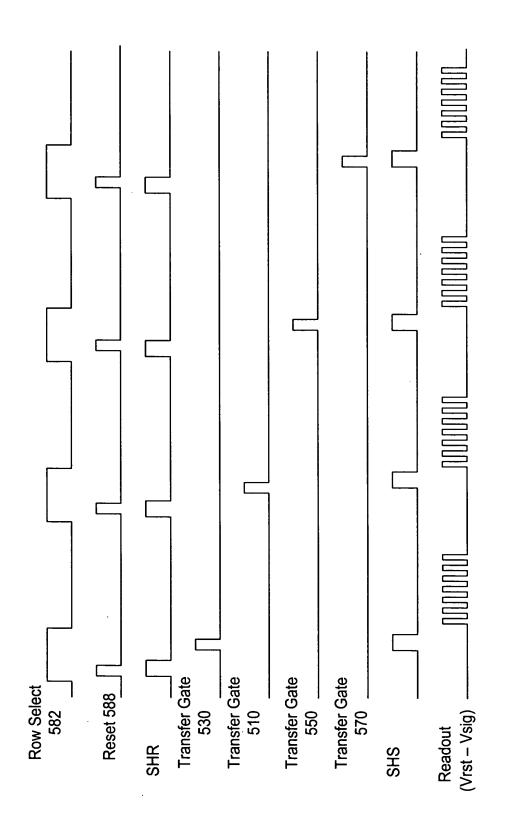


FIG. 8

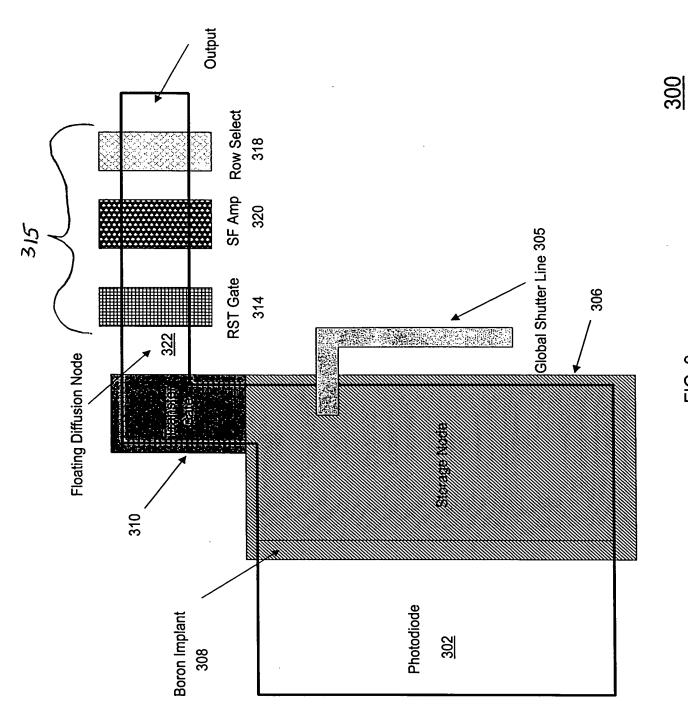


FIG. 9



